



European Hybrid IBIS Summit with IEEE SPI 2025

May 14, 2025

Agenda

All times CEST (UTC +2)

- 14:00 **SIGN IN**
- 14:05 **Welcome**
Douglas Burns (SI-Clarity LLC, USA)
(Acting Chair, IBIS Open Forum)
- 14:10 **IBIS Chair's Report**
Douglas Burns (SI-Clarity LLC, USA)
(Acting Chair, IBIS Open Forum)
- 14:30 **IBIS Modeling Approach for Designs having High Ron Variation with Process**
Rahul Kumar (STMicroelectronics, India)
Manish Bansal (STMicroelectronics, India)
Raushan Kumar (STMicroelectronics, India)
[Presented by Raushan Kumar]
- 14:45 **Bridging the Gap: Correlating IBIS-AMI Simulations with Post-Silicon Measurements for a 6.25 Gbps Transmitter**
Rahul Kumar (STMicroelectronics, India)
Manish Bansal (STMicroelectronics, India)
Anil Kumar Dwivedi (STMicroelectronics, India)
Kirtiman Singh Rathore (STMicroelectronics, India)
Rhani Menzer (STMicroelectronics, France)
[Presented by Rahul Kumar]
- 15:05 **Power Integrity Modeling in IBIS**
Arpad Muranyi (Siemens EDA, USA)
Walter Katz (The Mathworks, USA)
- 15:30 **ATM Task Group Report**
Arpad Muranyi (Siemens EDA, USA)
Chair, ATM Task Group
- 15:40 **IBIS Quality Task Group Report – IBISCHK8 Syntax Parser**
Randy Wolff (Siemens EDA, USA)
Chair, IBIS Quality Task Group
- 16:00 **BREAK (20 minutes)**

- 16:20 **IBIS Interconnect Task Group Update: Touchstone 3.0 Features & Progress**
Michael Mirmak (Intel, USA)
Chair, Interconnect Task Group
- 16:35 **IBIS-AMI Modeling Formulation for Bi-Directional Multi-GBased-T1 Automotive Ethernet Links**
Fangyi Rao (Keysight Technologies, USA)
Zhiping Yang, (PCB Automation Inc. & MST EMC Lab, USA)
Saied Benyamin (Ethernovia Inc., USA)
Geoff Zhang (AMD, USA)
[Presented by Zhiping Yang]
- 17:00 **OPEN DISCUSSION**
- 17:15 **CLOSING REMARKS**
- Next IBIS Open Forum Meeting Friday, May 16, 2025
- 17:20 **END OF SUMMIT**
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